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Application Number		10806633	
Filing Date		2004-03-22	
First Named Inventor Dimpr		sey et al.	
Art Unit		2188	
Examiner Name Moore Attorney Docket Number		e, Patrick M.	
		AUS920040062US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	5761103		1998-06-02	Oakland et al.	
	2	6549959	B1	2003-04-13	Yates et al.	
	3	6772322	B1	2004-08-03	Merchant et al.	
	4	5937437		1999-08-10	Roth et al.	
	5	6070009		2000-05-30	Dean et al.	
	6	6381679	B1	2002-04-30	Matsubara et al.	
	7	6687794	B2	2004-02-03	Malik	
	8	6539458	B2	2003-03-25	Holmberg	

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Schmuck et al.

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B1 2005-12-06

	10	6782454	B1	2004-08	3-24	Damron					
	11	5652858		1997-06	5-29	Okada et al.					
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		publisher, city androi country where published.	ı
	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000005888D	
	2	*Cache Miss Director - A Means of Prefetching Cache Missed Lines*, August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp. 1286	

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	Art Unit		2188	
Examiner Name Moore		Moore	e, Patrick M.	
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